

# Programmable Pulse Generator

**SERIES: PPG-312F  
(12 Bit) TTL Interfaced**

**data delay devices, inc.**



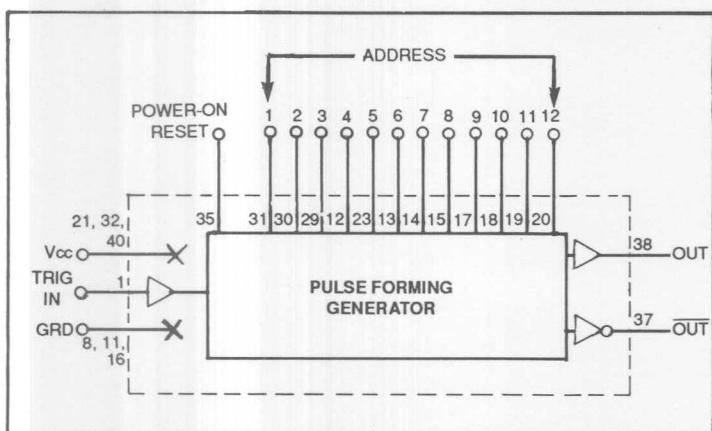
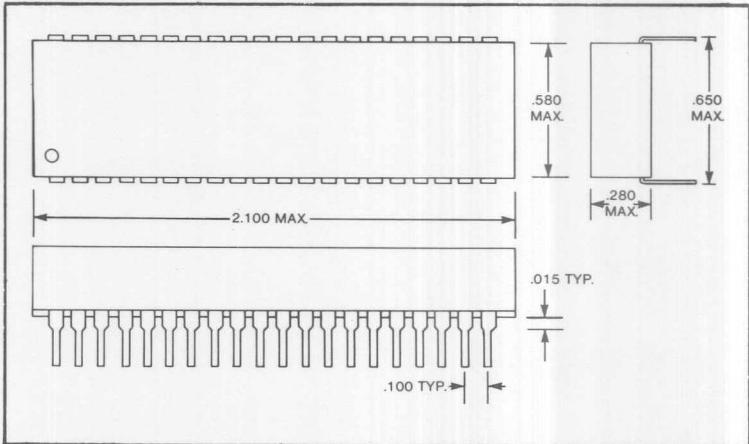
## Features:

- Precise pulse width.
- Inverted & non-inverted outputs.
- 12 BIT address.
- .5 ns to 10 ns incremental steps.
- Rising-edge triggered.
- 40 pins DIP package.
- Low profile.



## Specifications:

- Input requirement: TTL rising-edge.
- Output fan-out: TTL Schottky loads.
- Trigger inherent delay: 13 ns typ.
- Inherent pulse-width: 19 ns typ.
- Pulse-width variation: monotonic in one direction.
- Programmed pulse-width tolerance:  $\pm 2\%$  or 2 ns whichever is greater.
- Supply voltage ( $V_{cc}$ ): 5 Vdc.
- Operating temperature: 0° C to 70° C.
- Temperature coefficient: 100 PPM/° C.
- Supply current:  
 $I_{cc}$ : 200 ma.



Part Number	Incremental Pulse-Width (ns)	Total Programmed Pulse-Width (μs)
PPG-312F-2	2 ± .5	8.192
PPG-312F-3	3 ± 1.0	12.288
PPG-312F-4	4 ± 1.0	16.384
PPG-312F-5	5 ± 1.5	20.480
PPG-312F-6	6 ± 1.5	24.576
PPG-312F-7	7 ± 1.5	28.672
PPG-312F-8	8 ± 2.0	32.768
PPG-312F-9	9 ± 2.0	36.864
PPG-312F-10	10 ± 2.0	40.960
PPG-312F-20	20 ± 2.0	81.920
PPG-312F-30	30 ± 2.0	122.880
PPG-312F-40	40 ± 2.0	163.840
PPG-312F-50	50 ± 2.5	204.800